

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants: R. Bhattacharya et al.

Case: 1-4-2-2-1

Serial No.: 10/620,045

Filing Date: July 15, 2003

Group: 2123

Examiner: Juan Carlos Ochoa

Title: Method and Apparatus for Automatic Generation
of Multiple Integrated Circuit Simulation Configuration

AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the final Office Action dated May 10, 2010, Applicants respectfully request reconsideration of the above-identified application in view of the amendments and remarks herein.

OK TO ENTER: /J.O./